

12/26/01  
JC974 U.S. PTO

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PTO/SB/05 (08-00)

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# UTILITY PATENT APPLICATION TRANSMITTAL

(Only for new nonprovisional applications under 37 CFR 1.53(b))

Attorney Docket No.	Chittipeddi 89-14
First Inventor	Sailesh Chittipeddi
Title	CMOS Vertical Replacement Gate (VRG) Transistors
Express Mail Label No.	ET145592615US

## APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

- ☒ Fee Transmittal Form (e.g., PTO/SB/17)  
(Submit an original and a duplicate for fee processing)
- ☐ Applicant claims small entity status.  
See 37 CFR 1.27.
- ☒ Specification [Total Pages **30**]  
(preferred arrangement set forth below)
  - Descriptive title of the invention
  - Cross Reference to Related Applications
  - Statement Regarding Fed sponsored R & D
  - Reference to sequence listing, a table, or a computer program listing appendix
  - Background of the Invention
  - Brief Summary of the Invention
  - Brief Description of the Drawings (if filed)
  - Detailed Description
  - Claim(s)
  - Abstract of the Disclosure
- ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets **15**]
- Oath or Declaration [Total Pages **4**]
  - ☐ Newly executed (original or copy)
  - ☐ Copy from a prior application (37 CFR 1.63 (d))  
(for continuation/divisional with Box 17 completed)
  - ☐ **DELETION OF INVENTOR(S)**  
Signed statement attached deleting inventor(s)  
named in the prior application, see 37 CFR 1.63(d)(2) and 1.33(b).
- ☐ Application Data Sheet. See 37 CFR 1.76

ADDRESS TO: Assistant Commissioner for Patents  
Box Patent Application  
Washington, DC 20231

- ☐ CD-ROM or CD-R in duplicate, large table or Computer Program (Appendix)
- Nucleotide and/or Amino Acid Sequence Submission (if applicable, all necessary)
  - ☐ Computer Readable Form (CRF)
  - Specification Sequence Listing on:
    - ☐ CD-ROM or CD-R (2 copies); or
    - ☐ paper
  - ☐ Statements verifying identity of above copies

## ACCOMPANYING APPLICATION PARTS

- ☐ Assignment Papers (cover sheet & document(s))
- ☐ 37 CFR 3.73(b) Statement (when there is an assignee) ☐ Power of Attorney
- ☐ English Translation Document (if applicable)
- ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS Citations
- ☐ Preliminary Amendment
- ☒ Return Receipt Postcard (MPEP 503)  
(Should be specifically itemized)
- ☐ Certified Copy of Priority Document(s)  
(if foreign priority is claimed)
- ☒ Other: **Express Mailing Certificate**

17. If a CONTINUING APPLICATION, check appropriate box, and supply the requisite information below and in a preliminary amendment, or in an Application Data Sheet under 37 CFR 1.76:

<input type="checkbox"/> Continuation	<input type="checkbox"/> Divisional	<input type="checkbox"/> Continuation-in-part (CIP)	of prior application No.: _____
---------------------------------------	-------------------------------------	---	---------------------------------

Prior application information: Examiner \_\_\_\_\_ Group / Art Unit: \_\_\_\_\_  
For CONTINUATION OR DIVISIONAL APPS only: The entire disclosure of the prior application, from which an oath or declaration is supplied under Box 5b, is considered a part of the disclosure of the accompanying continuation or divisional application and is hereby incorporated by reference. The incorporation can only be relied upon when a portion has been inadvertently omitted from the submitted application parts.

## 18. CORRESPONDENCE ADDRESS

☐ Customer Number or Bar Code Label ☒ Correspondence address below  
(Insert Customer No. or Attach bar code label here)

Name	John L. DeAngelis, Jr., Esquire				
Address	Holland & Knight LLP 1499 S. Harbor City Blvd., Suite 201				
City	Melbourne	State	FL	Zip Code	32901
Country	US	Telephone	(321) 674-1778	Fax	(321) 951-1849

Name (Print/Type)	John L. DeAngelis, Jr.	Registration No. (Attorney/Agent)	30,622
Signature			Date Dec 26 2001

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Box Patent Application, Washington, DC 20231.

# FEE TRANSMITTAL for FY 2002

Patent fees are subject to annual revision.

TOTAL AMOUNT OF PAYMENT (\$ 812.00)

## Complete if Known

Application Number	Unassigned
Filing Date	Simultaneously Herewith
First Named Inventor	Sailesh Chittipeddi
Examiner Name	Unassigned
Group Art Unit	Unassigned
Attorney Docket No.	Chittipeddi 89-14

## METHOD OF PAYMENT

1. ☒ The Commissioner is hereby authorized to charge indicated fees and credit any overpayments to:

Deposit Account Number **501735**  
Deposit Account Name **Agere Systems**

- ☒ Charge Any Additional Fee Required Under 37 CFR 1.16 and 1.17  
☐ Applicant claims small entity status. See 37 CFR 1.27

2. ☐ Payment Enclosed:

☐ Check ☐ Credit card ☐ Money Order ☐ Other

## FEE CALCULATION

### 1. BASIC FILING FEE

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
101 740	201 370	Utility filing fee	740.00
106 330	206 165	Design filing fee	
107 510	207 255	Plant filing fee	
108 740	208 370	Reissue filing fee	
114 160	214 80	Provisional filing fee	

SUBTOTAL (1) (\$ 740.00)

### 2. EXTRA CLAIM FEES

Total Claims **24** -20\*\* = **4** X **18.00** = **72.00**  
Independent Claims **3** -3\*\* = **0** X  =   
Multiple Dependent  X  =

Large Entity Fee Code (\$)	Small Entity Fee Code (\$)	Fee Description
103 18	203 9	Claims in excess of 20
102 84	202 42	Independent claims in excess of 3
104 280	204 140	Multiple dependent claim, if not paid
109 84	209 42	** Reissue independent claims over original patent
110 18	210 9	** Reissue claims in excess of 20 and over original patent

SUBTOTAL (2) (\$ 72.00)

\*for number previously paid, if greater; For Reissues, see above

## FEE CALCULATION (continued)

### 3. ADDITIONAL FEES

Fee Code	Large Entity Fee (\$)	Small Entity Fee Code (\$)	Fee Description	Fee Paid
105 130	205 65		Surcharge - late filing fee or oath	
127 50	227 25		Surcharge - late provisional filing fee or cover sheet	
139 130	139 130		Non-English specification	
147 2,520	147 2,520		For filing a request for ex parte reexamination	
112 920*	112 920*		Requesting publication of SIR prior to Examiner action	
113 1,840*	113 1,840*		Requesting publication of SIR after Examiner action	
115 110	215 55		Extension for reply within first month	
116 400	216 200		Extension for reply within second month	
117 920	217 460		Extension for reply within third month	
118 1,440	218 720		Extension for reply within fourth month	
128 1,960	228 980		Extension for reply within fifth month	
119 320	219 160		Notice of Appeal	
120 320	220 160		Filing a brief in support of an appeal	
121 280	221 140		Request for oral hearing	
138 1,510	138 1,510		Petition to institute a public use proceeding	
140 110	240 55		Petition to revive - unavoidable	
141 1,280	241 640		Petition to revive - unintentional	
142 1,280	242 640		Utility issue fee (or reissue)	
143 460	243 230		Design issue fee	
144 620	244 310		Plant issue fee	
122 130	122 130		Petitions to the Commissioner	
123 50	123 50		Processing fee under 37 CFR 1.17(q)	
126 180	126 180		Submission of Information Disclosure Stmt	
581 40	581 40		Recording each patent assignment per property (times number of properties)	
146 740	246 370		Filing a submission after final rejection (37 CFR § 1.129(a))	
149 740	249 370		For each additional invention to be examined (37 CFR § 1.129(b))	
179 740	279 370		Request for Continued Examination (RCE)	
169 900	169 900		Request for expedited examination of a design application	

Other fee (specify) \_\_\_\_\_

\*Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$)

## SUBMITTED BY

Name (Print/Type) **John L. DeAngelis, Jr.**  
Signature **[Signature]**

Registration No. **30,622**  
(Attorney/Agent)

## Complete (if applicable)

Telephone **(321) 674-1778**

Date **Dec 26 2001**

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.

VIA EXPRESS MAIL ET145592615US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: )  
Applicant: Sailesh Chittipeddi )  
Appl. No.: Not Assigned )  
Filing Date: Simultaneously Herewith )  
For: CMOS Vertical Replacement )  
Gate (VRG) Transistors )

Commissioner of Patents  
Box Patent Application  
Washington, D.C. 20231

Sir:

EXPRESS MAIL # ET145592615US

Date of Deposit: December 26, 2001

I hereby certify that the attached Patent Application, formal papers and informal drawings were deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to Commissioner of Patents, Box Patent Application, Washington, D.C. 20231.

Respectfully submitted,

Pamela A. D'Alessio

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